## IN THE UNITED STATES DISTRICT COURT FOR THE WESTERN DISTRICT OF TEXAS WACO DIVISION

ACQIS LLC,

Plaintiff,

Case No. 6:22-cv-386-ADA

v.

SONY INTERACTIVE ENTERTAINMENT INC., SONY INTERACTIVE ENTERTAINMENT LLC,

Defendants.

JURY TRIAL DEMANDED

#### **JOINT CLAIM CONSTRUCTION STATEMENT**

Pursuant to this Court's Scheduling Order (Dkt. 42), Plaintiff ACQIS LLC and Defendants Sony Interactive Entertainment Inc. and Sony Interactive Entertainment LLC (collectively, the "Parties") submit this Joint Claim Construction Statement.

The Parties have jointly submitted, via Box Link provided by the Law Clerk, PDF versions of all as-filed claim construction briefing and exhibits (which include all five patents-in-suit).

#### I. THE PATENTS-IN-SUIT

The patents-in-suit are U.S. Patent Nos. 9,529,768 ("'768"), 9,703,750 ("'750"), 8,977,797 ("'797"), RE44,654 ("'654"), and RE45,140 ("'140"), which are submitted herewith as Exhibits 1-5, respectively, and were also submitted as exhibits with Defendants' Opening Claim Construction Brief (Dkt. 53) (Exhibits 1-5).

The asserted claims are as follows:

Patent	Claims
'797	33, 34
'768	1, 2, 13, 17
'750	1, 2, 5, 7, 10, 12, 21, 24, 31, 34, 35, 44
'654	20, 21
'140	14, 15, 17-19, 21, 30, 31, 34-36, 38

## II. AGREED CONSTRUCTIONS

Pursuant to the Scheduling Order (Dkt. 42), the Parties met and conferred to narrow the terms in dispute but were unable to agree as to any constructions.

# III. LIST OF DISPUTED TERMS FOR CONSTRUCTION

The Parties provide the following list of disputed terms for construction, in the order they are addressed in the Parties' claim construction briefing.

<b>Term for Construction</b>	Patents and Asserted Claims	Plaintiff's Proposed Construction	Defendants' Proposed Construction
"low voltage differential signal	'797 claims 33, [34] <sup>1</sup> ;	No construction necessary.	"[a channel for carrying] a
(LVDS) [channel]" / "LVDS	'768 claims 1, 2, 13, 17;	These terms should be given	signal in accordance with
channel"	'750 claims 1, 2, 5, 7, 10, [12],	their plain and ordinary	ANSI/TIA/EIA-644 or IEEE
	21, 24, 31, [34], 35, 44;	meaning.	1596.3" or, alternatively,
(proposed by Defendants)	'654 claims 20, 21;		indefinite.
	'140 claims 14, 15, 17-19,	These terms are not indefinite.	
	[21], 30, 31, 34-36, [38].		
"Peripheral Component	'768 claims 1, 2, 13, 17;	"a transaction, in accordance	"a transaction, in accordance
Interconnect (PCI) bus	'750 claims 1, 2, 5, [7], 10,	or backwards compatible with	with the industry standard PCI
transaction" / "PCI bus	[12], 21, 31, [34], 35, 44;	the industry standard PCI	Local Bus Specification, for
transaction"	'654 claim 21;	Local Bus Specification, for	communication with an
	'140 claims 30, [31], [34].	communication with an	interconnected peripheral
(proposed by Plaintiff and		interconnected peripheral	component"
Defendants)		component"	
"convey [conveying/conveys/	'768 claims 1, 2, 13, 17;	Other than "PCI bus	"communicating a PCI bus
communicating/communicate/	'750 claims 1, 2, 5, [7], 10,	transaction," addressed above,	transaction, including all
transmitting] a Peripheral	[12], 21, 31, [34], 35, 44;	these terms do not require	address, data, and control bits"
Component Interconnect (PCI)	'654 claim 21;	construction.	
bus transaction [/of a PCI bus	'140 claims 30, [31], [34].		
transaction]"		Specific recited bits of a PCI	
		bus transaction should be	
(proposed by Defendants)		given their plain and ordinary	
		meaning.	
"of a Peripheral Component	'768 claims 1, [2], 13, [17];	No construction necessary.	"from a transaction that is in
Interconnect (PCI) bus	'750 claims 1, [2], 5, [7], 10,	Putting aside "PCI bus	accordance with the industry
transaction" / "of a PCI bus	[12], 21, 31, [34], 35, 44;	transaction," addressed above,	standard PCI Local Bus
transaction"	'654 claim 21;	this phrase should be	Specification, for
	'140 claims 30, [31], [34].	broadened to include its full	communication with an

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<sup>&</sup>lt;sup>1</sup> Bracketed claims indicate dependent claims that do not explicitly recite the disputed term but depend from a claim that does recite the disputed term.

Term for Construction	Patents and Asserted Claims	Plaintiff's Proposed Construction	Defendants' Proposed Construction
(proposed by Defendants)		context—i.e., "address and data [bits] of a Peripheral Component Interconnect (PCI) bus transaction" / "address bits, data bits, and byte enable information bits of a Peripheral Component Interconnect (PCI) bus transaction"—and given its plain and ordinary meaning.  "[O]f a" does not require construction.	interconnected peripheral component"
Claims reciting an "encoded" PCI bus transaction (or, per ACQIS's position, specific bits thereof) in a "serial [bit] stream" or "serial form" or "serially encoded" and Claims reciting a PCI bus transaction (or, per ACQIS's position, specific bits thereof) in a "serial form" or "serial bit stream"  (proposed by Plaintiff and Defendants)	'768 claims 1, [2], 13, [17]; '750 claims 1, [2], 5, [7], 10, [12], 21, 31, [34], 35, 44; '654 claim 21; '140 claims 30, [31], [34].	"Encoding" terms, reciting conveying/transmitting encoded address and data bits of a PCI bus transaction or encoded address bits, data bits, and byte enable information bits, should be given their plain and ordinary meaning, wherein the plain and ordinary meaning of "encoded" is "code representing [the recited bits of] a PCI bus transaction."  "Serial" terms, reciting conveying/transmitting (1) address and data bits of a PCI bus transaction, or (2) address bits, data bits, and byte enable information bits, "in [a] serial	"a PCI bus transaction that has been serialized from a parallel form"

<b>Term for Construction</b>	<b>Patents and Asserted Claims</b>	Plaintiff's Proposed Construction	Defendants' Proposed Construction
		form," or "serially," or "in a serial bit stream," should be given their plain and ordinary meaning.	
		The EMC construction of "[e]ncoded serial bit stream of Peripheral Component Interconnect (PCI) bus transaction" and related terms applies only to certain claims and terms, i.e., "claims reciting an 'encoded' PCI bus transaction" and more specifically, terms that recite (1) "encoded," (2) "serial," and	
"console"  (proposed by Plaintiff and Defendants)	'750 claims 5, [7], 10, [12], 24, 35, 44; '654 claims 20, [21]; '140 claims 14, [15], [17], 18, [19], [21], 31, 34.	(3) "PCI bus transaction."  "a chassis or enclosure, housing one or more coupling sites, that connects components of a computer system"	"a chassis that connects several components of a computer system"
"USB" / "Universal Serial Bus (USB) protocol" / "Universal Serial Bus (USB) protocol data" / "Universal Serial Bus (USB) protocol information" (proposed by Plaintiff and Defendants)	'797 claims 33, [34]; '750 claims 7, 24; '654 claims 20, [21]; '140 claims 15, 18, [19], [21], 34, 36.	No construction necessary. These terms should be given their plain and ordinary meaning.	"[data/information conveyed according to] the protocols defined in the Universal Serial Bus Specification Revision 2.0 and the prior versions of this standard"

Term for Construction	Patents and Asserted Claims	Plaintiff's Proposed Construction	Defendants' Proposed Construction
"serial bit channels" / "serial channel"	'797 claims 33, [34]; '750 claims 10, [12]; '654 claims 20, [21];	No construction necessary. These terms should be given their plain and ordinary	"a path on which units of information are transferred serially from one component to
(proposed by Defendants)	'140 claims 14, [15], 17-19, [21], 30, 31, [34], 35, [36], [38].	meaning.	another"

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# **CERTIFICATE OF SERVICE**

I hereby certify that on July 12, 2023, I electronically filed the foregoing with the Clerk of Court using the CM/ECF system, which will send notification of such filing via electronic mail to all counsel of record. Any other counsel of record will be served by first class U.S. mail.

/s/ Logan J. Drew Logan J. Drew